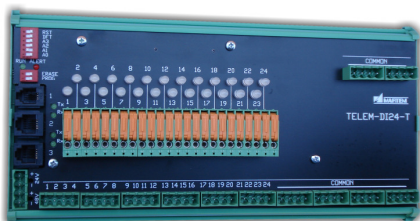


TELEM-DI24-T digital input RTU module



The TELEM family RTU module TELEM-DI24-T with 24 digital inputs is used for acquisition digital signals or counter pulses. Module bases on 32-bit ARM CPU. Interfaces to other equipment are RS-232 or RS-422. Data exchange protocol IEC 60870-5-1-101. Time synchronization is possible with synchronization pulse through GW6 device and over protocol.

Features

- Time synchronization with 1 ms accuracy
- 2 level input filters Buffer depth for each time tagged input is 20
- Logical operations with inputs
- Leased line or dial-up mode operation, data GSM communication request by event in substation or by remote control center
- Configuration/parameter setting with IEC protocol at the same line with data communication
- Daisy-chain master-slave connection for up to 15 modules using RS- 422 interface
- Self diagnostics and supervision simultaneously with data acquisition

Technical data

Number of digital inputs	24
Indication	1 for every input, 1 for running indication, 1 for alert indication
Built in clock with backup capacitor	Yes

Power requirements

Supply voltage	9...72 V DC, 1 W
Isolated input circuit voltage	20...135V DC, max 9 W @48 VDC
Input circuit isolation voltage to supply	2,5 kV RMS

Installation, terminals and environment

Weight	495 g
Mounting	DIN 35
Dimensions (WxHxD)	250 x 125 x 60
Terminals for signal	MVSTBWA 2,5/4-ST
Terminal for power	MVSTBWA 2,5/4-ST
Terminal for communication	RJ4/4 (RS232); RJ12 (RS422)
Over voltage protection	IEC-60255-4, 5 kV pulse protection IEC-60255-5, 2 kV DC on
Disturbance	IEC-61000-3-2, 61000-6-2, 6100-6-4
Ambient temperature in operation	-30...+70 °C

Configuration

Configuration of TELEM-DI24-T is configured using **TELEM-GWS** software. All 24 digital inputs are configured independently. Communication port's configurable parameters and general settings

Communication speed	200...115200 bps
Link Address	1...255
ASDU address	1...255
Object's base address	0...65500
Communication mode	Online
Time synchronization enabled	Yes
Buffer depth for each time tagged digital input	20

Configuration parameters for digital inputs

General

In use	Yes/No
Single- or double signal or counter input	Single/Double/Counter
Time tagged	Yes/No

Debouncing filter

Tolerant phase (A period of time during which changes of state are permitted and ignored on a digital input signal)	1...255 ms
Intolerant phase (A period of time during which the state of a digital input must remain constant)	1...255 ms

Chatter filter

A facility that is used to disable a digital input point if the number of state changes of that point during a defined time interval is excessively high.

Chatter times (changes)	1...255 times
Filter base period (time interval)	0...32000 ms
Lock-out period multiplier (number of filter base periods while the chatter filter will remain On)	1...255 periods

Counter

Number of pulses for creating an event	1...255 pulses
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